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Bib Data Sheet

CONFIRMATION NO. 3065

SERIAL NUMBER 09/898,986	FILING DATE 07/03/2001  RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. 07977/163003/US3375D1D1
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/272,701 03/18/1999 PAT 6,278,132  
 which is a CON of 08/890,591 07/08/1997 PAT 6,031,249

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 8-201294 07/11/1996

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 08/22/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 31	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Initials				

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## TITLE

Semiconductor device and method of manufacturing the same

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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